THE COMMISSIONER OF PATENTS AND TRADEMARKS

Washington, D.C. 20231

tansmitted herewith for filing is the Patent Application of:

inventor: MARK HATZILAMBROU, CHESTER LEUNG, RAJAN WALIA, LIEN WEE LIANG, SUBHASH C. rustagi, mk radhakrishnan

ESD PROTECTION SYSTEM FOR HIGH FREQUENCY APPLICATIONS

Enclosed are:

sheets of drawing(s) - formal.

An assignment of the invention to Institute of Microelectronics

An associate power of attorney

Applicant claims small entity status

Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

	(COI. 1)	(Col. 2)	Υ; :	SMALL ENTITY
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE				\$.3 45 .
TOTAL CLAIMS	35 -20=	15	x G =	\$ 135.
INDEP CLAIMS	2 -3=	0	× 40 =	\$ 0.
MULTIPLE DEPENDENT CLAIM PRESENTED + 260 =				
SUB TOTAL				\$ 510 .
ASSIGNMENT			\$40.	
		TO	TAL	s 550.
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The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.

Any additional filing fees required under 37 CFR \$1.16.

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EXPRESS MAIL CERTIFICATE

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I Hereby Certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to: The Commissioner of Patents and Trademarks, Washington, DC 20231. Applicant and/or Attorney requests the date of deposit as the Filing Date.

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